

Application No. 10/697,304
Amendment dated November 14, 2007
After Final Office Action of August 14, 2007

Docket No.: 0698-0165P

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AMENDMENTS TO THE CLAIMS

1-10. (Canceled)

11. (Currently Amended) A method for protecting an embedded software from being copied and used without authorization, applying on an electronic information appliance comprising a RAM and a NVRAM, the method the comprising steps of:

providing a power to the electronic information appliance;

receiving a quest with a plurality of parameters from a user to the embedded software;

storing the parameters to the RAM in a first type in a first address;

getting the parameters in a ~~first~~ the first type from the RAM;

changing the parameters in the first type to be in a second type and storing the parameters in the second type in the NVRAM; and

clearing the parameters in the first type in the RAM while the power is still provided to the electronic information appliance, wherein the step of changing the parameters in the first type to be in a second type is performed by changing a sequence of the parameters.

12. (Currently Amended) The method of claim 11, wherein the embedded software executes the quest with the parameters by getting the parameters in the second type ~~from the~~ from a second address of the storage device.

13. (Canceled)

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14. (Currently Amended) The method of claim 11, wherein the step of changing the parameters in the first type to the be in a second type changes-is further performed by coding a content of the parameters.

15. (Previously Presented) The method of claim 11, wherein the storage device is a buffer in a memory.

16. (New) A method for protecting an embedded software from being copied and used without authorization, applying on an electronic information appliance comprising a RAM and a NVRAM, the method comprising the steps of:

receiving a quest with a plurality of parameters from a user to the embedded software;
storing the parameters to the RAM in a first sequence;
getting the parameters in the first sequence from the RAM;
changing the parameters in a first sequence to be in a second sequence and storing the parameters in the second sequence in the NVRAM; and
clearing the parameters in the first sequence in the RAM.